SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

M.Tech I Semester (R20) Second Mid Term Examinations

TIME TABLE

Timings: 10.00 a.m. to 11.30 a.m.   **23rd January 2024**

Date	Structural Engineering	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning) (R21 Regulation)
27.01.2024 (Saturday)	Advanced Structural Analysis (CE3901)	CMOS VLSI Design (EC4901)	Advanced Data Structures and Algorithm Analysis (CS4901)
29.01.2024 (Monday)	Theory of Elasticity (CE3902)	Advanced Microcontrollers (EC4902)	Data Science (CS4902)
30.01.2024 (Tuesday)	Advanced R.C. Design (CE3903)	Testing and Testability of VLSI Circuits (EC4903)	Digital Image Processing (CS4903)
31.01.2024 (Wednesday)	Advancements in Concrete Technology (CE3907)	Parallel Processing (EC4908)	Internet of Things (CS4906)
01.02.2024 (Thursday)	Research Methodology & IPR (BA3901) (10.00 am to 11.30 am)		
	Constitution of India (BA3902) (2.00 pm to 3.30 pm)		

Note: Seating arrangement will be notified before the commencement of the examination by the respective departments.

DYP   **Vice Principal**
(Academics)

Copy to: HODs – CE/ECE/CSE/MBA/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)